



10/604,186

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**TRANSMITTAL LETTER**  
**(General - Patent Issued)**

Docket No.  
**BUR920020148US1**

Patentee(s):

Braceras et al.

U.S. Patent No.

6,897,674 *B2*

Issue Date

May 24, 2005

Title: **ADAPTIVE INTEGRATED CIRCUIT BASED ON TRANSISTOR CURRENT MEASUREMENTS**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

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\_\_\_\_\_  
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Dated: 06-13-2005

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P17A/REV04

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO : 6,897,674 *B2*

DATED : May 24, 2005

INVENTOR(S) : Braceras et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, Line 58: delete "nvention" and insert -- invention --

Column 2, Line 43: delete "LEFF" and insert -- L<sub>EFF</sub> --

Column 3, Line 48: delete "IDSAT" and insert -- I<sub>DSAT</sub> --

Column 4, Line 20: delete "DSAT" and insert -- I<sub>DSAT</sub> --

Column 4, Line 31: delete "IDSAT" and insert -- I<sub>DSAT</sub> --

Column 5, Line 5: delete "1851" and insert -- 185I --

Column 5, Line 6: delete "1901" and insert -- 190I --

Column 5, Line 8: delete "1901" and insert -- 190I --

Column 9, Line 9: delete "aturation" and insert -- saturation --

Column 9, Line 9: delete "one or more one or more" and insert -- one or more --

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PATENT NO. 6,897,674 *B2*

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